

SEPARTMENT OF COMMERCE Patent and Trademark Offic

Address:

COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. |
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09/262,458

03/04/99

POSSLEY

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42390.P6643

MMC1/0426

HOWARD A SKAIST INTEL CORPORATION BLAKELY SOKOLOFF TAYLOR & ZAFMAN 12400 WILSHIRE BOULEVARD 7TH FLOOR LOS ANGELES CA 90025

| EXAMINER | | | | | | |
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| NGO, N | | | | | | |
| ART UNIT | PAPER NUMBER | | | | | |

2814

DATE MAILED:

04/26/00

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

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| Office Action Summary | Examiner | 7) 0 | 1 4 >) . | Group Art Unit | |
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| —The MAILING DATE of this communication appea | | | | ! | idross |
| | is on the covers | ileet D | eneaur uie cc | orrespondence ac | iui ess— |
| Period for Reply | 2) | | | | |
| A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO OF THIS COMMUNICATION. | O EXPIRE YV | س | MONTH(S) |) FROM THE MAIL | ING DATE |
| Extensions of time may be available under the provisions of 37 CFR 1 from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, a re If NO period for reply is specified above, such period shall, by default, Failure to reply within the set or extended period for reply will, by statute | ply within the statutor expire SIX (6) MONT | y minim | um of thirty (30) in the mailing date | days will be considere | ed timely. |
| Status | | | | | |
| Responsive to communication(s) filed on | 00 | | | | |
| ☑ This action is FINAL. | | | | | |
| ☐ Since this application is in condition for allowance except accordance with the practice under <i>Ex parte Quayle</i> , 193 | | | | the merits is clos | sed in |
| Disposition of Claims | | | | | |
| ✓ Claim(s) 1 10 26 | | | is/are p | pending in the appl | ication. |
| | Of the above claim(s) 13 To 20 | | | | |
| | - | | is/are v | withdrawn from cor | isideration. |
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| Claim(s) 1 ho 12 and 21 to | | | | | isideration. |
| □ Claim(s) 1 ho 12 and 21 to | 26 | | is/are a | allowed. rejected. | · |
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S. Patent and Trademark Office 326 (Rev. 9-97)

Part of Paper No.__

Serial Number: 09/262,458

Art Unit: 2814

The amendment filed April 3, 2000 has been entered and made of record as paper no. 8.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-12 and 21-26 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Tran et al (US-5,780,883, cited by Applicants).

Tran discloses in figures 3A and 3B a gate array architecture comprising a plurality of Ntype and P-type diffusion regions having polysilicon gates in which the regions have two distinct transistor sizes, smaller N and P type transistors and larger N and P type transistors. It would have been obvious that Tran discloses the claimed device.

In re claim 21, it is well-known to use gate array to form memory device (storage medium as claimed) such as taught by Tran on line 7 of column 2.

Claims 1-12 and 21-26 stand alternatively rejected under 35 U.S.C. 103(a) as being unpatentable over Sato (US-4,816, 887).

Sato discloses a gate array having two distinct transistor sizes on the order of one-third. It would have been obvious that Sato discloses the claimed device.

Applicant's arguments filed April 3, 2000 have been fully considered but they are not persuasive.

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Nothing in Tran discloses that the "non-overlying polysilicon landing sites" as argued by Applicants. In fact figures 3A and 3B show the polysilicon gates 54 overlying the diffusion regions 42, 44, 46, and 48.

Figure 8 of Sato also show the polysilicon gates 7G1, 7G2, 10G1, 10G2, and 3G1 overlying the diffusion regions.

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication should be directed to Examiner Ngan Ngo at telephone number (703) 308-4938. The fax phone number for the Art unit is (703) 308-7722.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956.

Serial Number: 09/262,458

Art Unit: 2814

Ngan Ngo

April 25, 2000

Ngan Van Ngo Primary Examiner